METHOD AND SYSTEM FOR CONDUCTING CONTINUITY TESTING ON ANALOG DEVICES HAVING SENSITIVE INPUT NODES

ABSTRACT

The present invention provides for a method (30) and system (10) for isolating the input nodes (3, 4) and/or the output nodes (5, 8) of an analog device (12) and performing continuity testing thereof without using relays. The system includes an analog device having a pair of input and output terminals and a plurality of resistors (R1-R3 and R4-R6) arranged in parallel and connected thereto. The method for testing continuity of the analog device includes providing a voltage input via at least one of the resistors to either input node, and then measuring the voltage at the same node via a resistor. If a diode drop from ground is sensed there is continuity, and if the applied voltage is sensed at the node there is not continuity. As a result, the invention advantageously isolates the nodes and removes any unwanted capacitance and impedance loading thereon during testing thereof. The invention also allows multiple node testing to be performed simultaneously in parallel, which reduces testing time and permits direct testing on the nodes without the use of external circuit relays.